

Application Serial No. 10/642,936
Reply to office action of April 5, 2005

PATENT
Docket: CU-3332

Amendments To The Claims

The listing of claims presented below will replace all prior versions, and listings, of claims in the application.

Listing of claims:

1. (Currently Amended) A method of fabricating a semiconductor chip from a semiconductor wafer having a first surface supporting a semiconductor element and a second surface opposite the first surface, the method comprising the steps of:
performing isotropic etching at least partially on a cutting portion of the semiconductor wafer from one or both of the first surface and the second surface,
thereby forming a groove having a bowl-shaped cross-section; and
~~performing anisotropic etching on a remaining portion of the cutting portion from the one or both of the first surface and the second surface a bottom surface of the groove, thereby cutting the cutting portion of separating the semiconductor wafer into individual semiconductor elements.~~
2. (Original) The method as claimed in claim 1, further comprising the step of:
forming a resist on the first surface to expose the cutting portion on the first surface, when the cutting portion is isotropically etched from the first surface.
3. (Original) The method as claimed in claim 2, wherein the resist has rounded-off corners.
4. (Original) The method as claimed in claim 1, further comprising the step of:

Application Serial No. 10/642,936
Reply to office action of April 5, 2005

PATENT
Docket: CU-3332

forming a resist on the second surface to expose the cutting portion on the second surface, when the cutting portion is isotropically etched from the second surface.

5. (Original) The method as claimed in claim 4, wherein the resist has rounded-off corners.

6. (Withdrawn) A semiconductor chip fabricated from a semiconductor wafer, comprising:

a first surface supporting a semiconductor element; and

a second surface opposite the first surface,

wherein at least one of the first surface and the second surface has rounded-off edges.

7. (Withdrawn) The semiconductor chip as claimed in claim 6, further comprising notched side surfaces.

8. (Withdrawn) A semiconductor chip fabricated from a semiconductor wafer, comprising:

a plurality of rounded-off corners.

9. (New) A method of fabricating a semiconductor chip from a semiconductor wafer having a first surface supporting a semiconductor element and a second surface opposite the first surface, the method comprising the steps of:

performing isotropic etching on a cutting portion of the semiconductor wafer from one of the first surface and the second surface, thereby forming a first groove having a bowl-shaped cross-section;

performing anisotropic etching on a bottom surface of the first groove, thereby

Application Serial No. 10/642,936
Reply to office action of April 5, 2005

PATENT
Docket: CU-3332

forming a second groove; and

performing isotropic etching on a cutting portion of the semiconductor wafer from the other surface, thereby forming a third groove coupled to the second groove, the third groove having a bowl-shaped cross-section, and thereby separating the semiconductor wafer into individual semiconductor elements.